

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10052259	FILING DATE 01/23/2002	CLASS 438	SUBCLASS 12	GAU 2812	EXAMINER THAD LE
**APPLICANTS: Shimooka Yoshiaki, Matsunaga Noriaki, Shibata Hideki,					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-372974 12/06/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO. 218357US2	
TITLE : Semiconductor device and method of manufacturing the same					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs.Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE Application Examiner	
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